

## COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE, the specification of which:

is attached hereto.

was filed on February 22, 2002 as Application Serial No. 10/069,229 and was amended on

was described and claimed in PCT International Application No. \_\_\_\_\_ filed on \_\_\_\_\_ and as amended under PCT Article 19 on \_\_\_\_\_.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information I know to be material to patentability in accordance with Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate or of any PCT international application(s) designating at least one country other than the United States of America listed below and have also identified below any foreign application for patent or inventor's certificate or any PCT international application(s) designating at least one country other than the United States of America filed by me on the same subject matter having a filing date before that of the application(s) of which priority is claimed:

Country	Application No.	Filing Date	Priority Claimed
WIPO	PCT/US00/23996	08/31/2000	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No <input type="checkbox"/> Yes <input type="checkbox"/> No

I hereby appoint the following attorneys and/or agents to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

Scott C. Harris, Reg. No. 32,030  
David L. Feigenbaum, Reg. No. 30,378  
Christina Sperry, Reg. No. 47,106  
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patents issued thereon.

**Combined Declaration and Power of Attorney**  
Page 2 of 8 Pages

1-w  
Full Name of Inventor:

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Inventor's Signature:

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5-w  
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Inventor's Signature:

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**POWER OF ATTORNEY  
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**PATENT COOPERATION TREATY  
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The undersigned applicant:

INTEL CORPORATION

hereby appoints

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as common representative(s)

to act on applicant's behalf before the competent International Authorities in connection with  
the international application on

Title: BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE  
Agent's Reference: 10559-305US1  
Application Number: 10/069,229

filed with the United States receiving Office and to make or receive payments on applicant's  
behalf.

Place: \_\_\_\_\_

Date: \_\_\_\_\_

Signature: \_\_\_\_\_  
INTEL CORPORATION

Printed Name: \_\_\_\_\_

Title: \_\_\_\_\_

**POWER OF ATTORNEY  
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APPOINTMENT OF AGENT OR COMMON REPRESENTATIVE**

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Agent's Reference: 10559-305US1

Application Number: 10/069,229

filed with the United States receiving Office and to make or receive payments on applicant's  
behalf.

Place: Hudson, MA. US.

Date: June 19, 2002

Signature: Gilbert Wolrich  
GILBERT WOLRICH

Printed Name: GILBERT WOLRICH

Title: Inventor

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behalf.

Place: Hudson, Ma

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behalf.

Place: Hudson MA USA

Date: 6/19/02

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